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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a 371 of PCT/JP01/03922 05/11/2001

*verified* *rs*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2000-140072 05/12/2000

*wi pd* *rs*

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature: <i>[Signature]</i> Initials: <i>[Initials]</i>	JAPAN	16	14	7

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## TITLE

Semiconductor device, mounting circuit board, method of producing the same, and method of producing mounting structure using the same

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